IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Koichi MORITA) Art Unit: 2816
Serial No. 10/522,264 (National Phase of PCT/JP2004/007756)) Examiner: Dinh Thanh Le
Filed: January 25, 2005)
For: Semiconductor Switch)Attorney Docket No.: 44471/311601)
	CERTIFICATE OF ELECTRONIC FILING I hereby certify that this correspondence is being electronically filed with the United States Patent Office via EFS Web on September 15, 2006 Janie Wilkins

INFORMATION DISCLOSURE STATEMENT

Mail Stop PCT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The citation of information on the attached Form PTO/SB/08a, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each non-U.S. patent document listed is enclosed.

Cite Nos. 1-3 of the references listed in the Form PTO/SB/08a were cited in an Official Action Issued May 16, 2006 in a counterpart Japanese application. A copy of the Japanese Office Action (in Japanese) is enclosed.

The citation of this information does not constitute an admission of priority or that any cited item is available as a reference, or a waiver of any right the applicant may have under applicable statutes, Rules of Practice in patent cases, or otherwise.

No fee is believed due since the Supplemental Information Disclosure

Statement was filed before the issuance of a first Office Action on the merits. If a fee is due

Information Disclosure Statement
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the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account 11-0855.

Respectfully submitted,

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Respectfully submitted,

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